

Docket No.: 60188-656

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Hiroshi TAKENAKA :
Serial No.: Divisional of Appl. No.
09/812,947 : Group Art Unit:
Filed: January 06, 2004 : Examiner:
For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE COMPRISING A
PLURALITY OF SEMICONDUCTOR DEVICES FORMED ON A SUBSTRATE (AS
AMENDED)

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as follows: